

Docket No.: GR 98 P 1507

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By: Markus Nollf Date: November 7, 2001

Inventor : Andreas Rusch et al.
Applic. No. : 09/685,361
Filed : October 10, 2000
Title : Semiconductor Memory Device and Method for Fabricating It
Examiner : Lynne Gurley - Art Unit: 2812

RESPONSE

Hon. Commissioner of Patents and Trademarks,
Washington, D. C. 20231

Sir:

Responsive to the Office Action dated October 25, 2001, the following remarks are made:

In deference to the restriction requirement on pages 2 and 3 of the above-identified Office Action, applicants elect group I, Claim 2 for prosecution at this time.

Applicants reserve the right to file a divisional application including the product claim of group II at a later date.

In view of the foregoing, the early issuance of an Action on the merits of claims is solicited.

Respectfully submitted,

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For Applicants

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Date: November 7, 2001

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